

TUNABLE MULTI-BAND ANTENNA ARRAY

CROSS REFERENCE TO RELATED APPLICATIONS

5 Not Applicable.

STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH

This invention was made with government support under Contract No. F19628-00-C-002 awarded by the United States Air Force. The government has certain rights in the
10 invention.

FIELD OF THE INVENTION

This invention relates generally to antennas and more particularly to an antenna element and an antenna array that can operate in two or more frequency bands.
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BACKGROUND OF THE INVENTION

A variety of conventional antennas are used to provide operation over selected frequency regions of the radio frequency (RF) frequency band. Notably, stacked patch antenna arrays have been used to provide simultaneous operation in two or more RF frequency bands.
20 Antenna array arrangements operating in two or more RF frequency bands can require complex mechanism and techniques to allow arrangements to be selectively tuned to the two or more frequency bands.

Existing stacked patch antenna elements that have been adapted to operated in two RF
25 frequency bands sometimes use air gaps disposed between dielectric layers to tune each of the frequency bands. This technique provides dual-band stacked patch antenna elements for which fine tuning is very difficult. The technique also provides antenna elements that can achieve only a relatively small difference in the frequency between each of the two frequency bands. In contrast, some applications, for example global positioning system (GPS) applications, have
30 two operating frequencies (designated herein as L1 and L2) that have relatively wide separation.

It will be recognized that a conventional GPS system provides L1 at 1575.42 MHz and L2 at 1227.60 MHz, each having a bandwidth of 24 MHz. An antenna that can provide a relatively large frequency separation is desirable.

5 Conventional antenna arrays are provided having a plurality of antenna elements. Coupling between respective ones of the plurality of elements can produce undesired antenna and system effects, for example, unwanted beam pattern behavior, and unwanted coupling between transmitting and receiving elements. Thus, it is desirable in an antenna array having a plurality of antenna elements to reduce the amount of coupling between respective ones of the
10 plurality of antenna elements.

For GPS applications, microstrip antenna arrays have been provided having a plurality of microstrip elements. Conventional microstrip designs suffer from a relatively high amount of coupling due to surface wave interference between elements.

15 It would, therefore, be desirable to provide a multi-band antenna array arrangement, wherein respective antenna elements associated with each frequency band are selectively tunable, and wherein the frequency bands can have a relatively large frequency separation. It would be further desirable to provide a multi-band antenna array arrangement having a plurality
20 of antenna elements that are electrically and electro-magnetically isolated from each other.

SUMMARY OF THE INVENTION

In accordance with the present invention, an antenna is provided having a substrate, a plurality of antenna elements disposed on one surface thereof, and a ground plane disposed on
25 the other surface. A surface wave control structure is provided between antenna elements to decouple the antenna elements from each other. The surface wave control structure has an apex that provides a sharp edge.

With this particular arrangement, antenna elements combined within an antenna array are
30 greatly decoupled from each other. System performance, including beam pattern shape, are

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FIG. 4-4D are cross section views of exemplary tuning arrangements in accordance with the present invention applied to a variety of stacked patch antenna elements; and

FIG. 5 is a schematic representation of a combiner circuit applied to the antenna array of FIG. 1.

DETAILED DESCRIPTION OF THE INVENTION

Referring now to FIG. 1, an antenna array 10 includes a substrate 12 having first and second opposing surfaces 12a, 12b. The substrate 12 is provided as a dielectric material such as fiberglass, PTFE, or the like. Disposed on the first surface of the substrate are a plurality of antenna elements 14a-14d. The elements 14a-14d are here shown as patch elements although other shaped elements (e.g. rectangular, round or even irregular shaped elements) may also be used.

First and second surface wave control structures 16a, 16b are disposed between the antenna elements 14a-14d to minimize the mutual coupling between the radiating elements 14a-14d. It should be appreciated that the surface wave control structures 16a, 16b must be provided from a conductive material (e.g. aluminum, copper, or any other appropriate material including electrical material which can be plated) and that the surface wave control structures 16a, 16b may be fabricated by machining or any other technique well known to those of ordinary skill in the art. A ground plane 20 is disposed over the second surface 12b of the substrate 12.

Antenna element feeds 18a-18h are provided as points to which RF signals can be applied to the antenna elements 14a-14d. Tuning structures, denoted as tuning structure groups 22a-22d, are provided to tune the antenna element. The antenna feeds 18a-18h and the tuning structures 22a-22d will be further described in association with FIG. 3.

While the surface wave control structures 16a, 16b are shown having a particular orientation with respect to the antenna elements 14a-14d, it should be appreciated that other

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The height H and apex angle θ of the surface wave control structure are selected in accordance with a variety of factors, including but not limited to the antenna operating frequency, the separation, size and type of the antenna elements (e.g. antenna elements 14a-14d of Figure 1), the relative orientation of the antenna elements, and the available height of the antenna.

Referring now to FIG. 3, an exemplary dual stacked patch antenna element 50 includes one or more upper tuning structures 52, each provided having a diameter d_1 , and a first and a second end coupled respectively to an upper patch element 54 and to a lower patch element 56.

The antenna element 50 also includes one or more lower tuning structures 58a, 58b, each provided having a diameter d_2 , and a first and a second end coupled respectively to the lower patch element 56 and to a ground plane 60, for example, to the ground plane 20 of FIG. 1. One or more upper dielectric layers 62a-62c provide an isolation structure 62 between the upper patch element 54 and the lower patch element 56. The lower patch element 56 is disposed upon a first surface of the substrate 64, e.g. surface 12a of FIG. 1, and the ground plane 60 is disposed upon the second surface of the substrate 64, e.g. surface 12b of FIG. 1.

In one exemplary embodiment, the upper dielectric layer 62a is provided having a thickness of 60 mils and a dielectric constant of 2.94, the upper dielectric layer 62b is provided having a thickness of 30 mils and a dielectric constant of 2.2, the upper dielectric layer 62c is provided having a thickness of 10 mils and a dielectric constant of 2.94, and the substrate 64 is provided having a thickness of 310 mils and a dielectric constant of 2.94. In this particular embodiment, the upper tuning structure 52 and the lower tuning structures 58a, 58b are provided having a diameter of 32 mils. Also, in this particular embodiment, the upper patch element is square having sides of 2.216 inches and the lower patch element is square having sides of 2.580 inches.

A plated side wall 66, coupled to the ground plane 60, can be provided having an extension h_1 in association with the substrate 64. A non-conductive center pin 53 can be provided to align the antenna. A feed pin 68 can provide an electrical coupling to the upper

patch element 54 at a feed 55. Feed 55 corresponds to one of the feed points 18a-18h shown in FIG. 1. The upper patch element 54 and the lower patch element 56 can be provided having coupling features, of which coupling feature 63 is but one example, that provide a coupling to a respective end of the tuning structures, for example lower tuning structure 58b.

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In one exemplary embodiment, the plated side wall extension h1 is 120 mils. While the plated side wall 66 is shown in association with a single antenna element 50, it should be appreciated that the plated side wall can be associated with a plurality of antenna elements, wherein the plated side wall 66 can be disposed around the outside circumferential edge of the substrate, for example substrate 12 of FIG. 1. The plated side wall 66 provides improved impedance matching, or coupling, of the type described below.

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It will be recognized that, for this particular arrangement, the feed pin 68 provides a signal path to the upper patch element 54. In one particular embodiment, the upper patch element 54 has a first pre-determined capacitive and electro-magnetic coupling at a first signal frequency to the lower patch element, and the lower patch element 56 has a second pre-determined capacitive and electro-magnetic coupling at a second signal frequency to the ground plane 60. At the first signal frequency, the lower patch element 56 is provided having a low impedance to the ground plane 60, and at the second signal frequency the upper patch element 54 is provided having a low impedance to the lower patch element 56. Thus, at the first signal frequency, the upper patch element 54 receives the first signal frequency from the feed 68 and the lower patch element 56 acts as a ground plane. Similarly, at the second signal frequency, the lower patch element 56 receives the second signal frequency from the feed 68 by way of the low impedance coupling between the upper patch element 54 and the lower patch element 56, and the ground plane 60 acts as a ground plane. With this particular arrangement, the dual stacked patch antenna element 50 can operate at two RF frequencies.

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The tuning structures 52, 58a, 58b provide selective antenna tuning. At the first signal frequency where the lower patch element 56 acts as a ground plane for the first patch element 54, the upper tuning structure 52 provides antenna tuning. At the second signal frequency

where the ground plane 60 acts as a ground plane for the lower patch element 56, the lower tuning structures 58a, 58b provide antenna tuning.

The tuning of the upper patch element 54 at the first signal frequency is influenced by a variety of factors, including the number of the upper tuning structures 52, the placement of the upper tuning structures 52 about the upper patch element 54, the diameter d1 of the upper tuning structures 52, and the alignment of the upper tuning structures 52 with the feed 55 and with each other. The tuning of the lower patch element 56 at the second signal frequency is also influenced by a variety of factors, including the number of the lower tuning structures 58a, 58b, the placement of the lower tuning structures 58a, 58b about the lower patch element 56, the number of the lower tuning structure 58a, 58b, and the alignment of the lower tuning structures 58a, 58b with the feed 55 and with each other. The alignment of the tuning structures is described more fully below in association with FIG. 3A.

The upper and lower tuning structures 52, 58a, 58b can be provided in a variety of ways, including screws, rivets, plated through holes, or any electrically conductive structure. The diameters d1 and d2 can be equal or different. While the diameters d1, d2 are optimally within the range of 25 to 50 mils, other diameters d1, d2 can also be used with this invention.

With this particular arrangement, the tuning provided by the upper tuning structures 52 at the first signal frequency is essentially independent of the tuning provided by the lower tuning structures 58a, 58b at the second signal frequency. While a first and a second signal frequency have been described, it should be appreciated that the discussions herein apply equally well to a first frequency band and a second frequency band.

While one feed 55 is shown, it will be recognized that a variety of feeds to either or both of the upper patch element 54 and/or the lower patch element 56 can be provided with this invention. A variety of alternative patch and feed arrangements are shown below in association with FIGS. 4-4D.

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116, each provided having a first and a second end coupled respectively to the lower patch element 106 and to the ground plane 108.

Referring now to FIG 4A, an illustrative example of a dual stacked patch antenna element 150 is provided having an upper patch element 152, and a lower patch element 154. An isolation structure 153 is disposed between the upper patch element 152 and the lower patch element 154. A substrate 155 is disposed between the lower patch element 154 and a ground plane 156. A first upper patch feed 160 is coupled to the upper patch element 152, and a first lower patch feed 158 is coupled to the lower patch element 154.

The antenna element 150 includes one or more upper tuning structures 162, each having a first and a second end coupled respectively to the upper patch element 152 and the lower patch element 154. The antenna element 150 also includes one or more lower tuning structures 164, each provided having a first and a second end coupled respectively to the lower patch element 154 and to the ground plane 156.

Referring now to FIG 4B, another illustrative example of a dual stacked patch antenna element 200 is provided having an upper patch element 202, and a lower patch element 204. An isolation structure 203 is disposed between the upper patch element 202 and the lower patch element 204. A substrate 205 is disposed between the lower patch element 204 and a ground plane 206. An upper patch feed 210 is coupled to the upper patch element 202, and a lower patch feed 208 is coupled to the lower patch element 204.

The antenna element 200 includes one or more upper tuning structures 212, each having a first and a second end coupled respectively to the upper patch element 202 and the lower patch element 204. The antenna element 200 also includes one or more lower tuning structures 214, each provided having a first and a second end coupled respectively to the lower patch element 204 and to the ground plane 206.

Referring now to FIG 4C, yet another illustrative example of a dual stacked patch

antenna element 250 is provided having an upper patch element 252, and a lower patch element 254. An isolation structure 253 is disposed between the upper patch element 252 and the lower patch element 254. A substrate 255 is disposed between the lower patch element 254 and a ground plane 256. An upper patch feed 258 is coupled to the upper patch element 252.

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The antenna element 250 includes one or more upper tuning structures 260, each having a first and a second end coupled respectively to the upper patch element 252 and the lower patch element 254. The antenna element 250 also includes one or more lower tuning structures 262, each provided having a first and a second end coupled respectively to the lower patch element 254 and to the ground plane 256.

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This particular embodiment will be recognized to correspond to the configuration described above in association with FIGS. 1-3.

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Referring now to FIG 4D, yet another illustrative example of a dual stacked patch antenna element 300 is provided having an upper patch element 302, and a lower patch element 304. An isolation structure 303 is disposed between the upper patch element 302 and the lower patch element 304. A substrate 305 is disposed between the lower patch element 304 and a ground plane 306. An lower patch feed 308 is coupled to the lower patch element 304.

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The antenna element 300 includes one or more upper tuning structures 310, each having a first and a second end coupled respectively to the upper patch element 302 and the lower patch element 304. The antenna element 300 also includes one or more lower tuning structures 312, each provided having a first and a second end coupled respectively to the lower patch element 304 and to the ground plane 306.

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Referring now to FIG. 5, a plurality of combiner circuits 330a-330d are coupled to a plurality of antenna elements 320a-320d at two feeds 322a-322d and 324a-324d respectively. Here, the antenna elements can be provided as dual stacked patch antenna elements as shown above in FIG. 1.

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